


REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
A	PR3032/3033	02-JUL-15	S M M

8. ALL VOLTAGES MEASURED WITH RESPECT TO SP:GROUND USING A DVM WITH AT LEAST 1MEG AC INPUT IMPEDANCE AND 10MEG DC INPUT IMPEDANCE. VOLTAGES MAY VARY +/- 20%.
- TEST CONDITIONS:
8 OHM RESISTIVE LOAD CONNECTED AT SPKR OUT (J3).
PANEL CONTROL SETTINGS: GAIN @ 12:00, ALL OTHER CONTROLS AT MAX (FULLY CW)
DC TESTS (TP1 TO TP15,TP26) - NO SIGNAL APPLIED
- AC TESTS (TP16 TO TP23): 5MILLIVOLT, 1kHz SINUSOIDAL SIGNAL APPLIED TO INPUT (J5)
9. TREBLE BOOST SWITCH OUT EXCEPT FOR TESTS AT TP22, TP24, TP25
10. LINE OUT TEST (TP23) - INSTALL OPEN 1/4 INCH PLUG IN J2

- FOR COMBO:
P35,P36 INSTALLED. JMP5 NOT INSTALLED.
- FOR HEAD:
JMP5 INSTALLED. P35,P36 NOT INSTALLED.
6. R20,21,31 & C20,23 NOT INSTALLED.
5. THIS SCHEMATIC IS FOR PCB FABRICATION P/N 7706169000 AND PCB ASSEMBLY P/N 7706164000 (COMBO) & 7708153000 (HEAD)
4. ALL DIODES ARE 1N4448.
3. ALL POLARIZED CAPACITORS IN uF, 20%; 50V MINIMUM.
2. ALL UNPOLARIZED CAPACITORS IN uF, 10% OR BETTER; 50V MINIMUM. (POWER SUPPLY BYPASS CAPACITORS ARE 20%).
1. ALL RESISTORS IN OHMS, 5%; 1/4W.

NOTES: (UNLESS OTHERWISE NOTED)

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CHECKED BY: _____		TITLE: SERVICE DIAGRAM, COMBINED (schematic)	
DATE: _____		BASSBREAKER 007	
APPROVED BY: _____		MAIN PCB	
DATE: _____		SIZE	DRAWING NUMBER
DRAWN: GIT'LO		B	7706211000
ENGR: GIT'LO		REV.	A
DATABASE FILE: Z3032S.SCH		RELEASE DATE: 02-JUL-15	
		SHEET: 1 OF 3	